

**Course Schedule for CSC295-01—Spring 2011**  
(Subject to Change)

| <b>Week</b> | <b>Dates</b> | <b>Contents (textbook chapters/topics)</b>                                   |
|-------------|--------------|--|
| 1           | 1/18—1/21    | Ch1. Introduction<br>Ch2. Data Representations                               |
| 2           | 1/24—1/28    | Ch3. Combinational Circuits<br>Ch4. Sequential Circuits                      |
| 3           | 1/31—2/4     | Ch4. Sequential Circuits<br>Ch5. Arithmetic Logic Unit                       |
| 4           | 2/7—2/11     | Ch5. Arithmetic Logic Unit   |
| 5           | 2/14—2/18    | Ch7. Instruction Set Architectures<br>RISC/CISC                              |
| 6           | 2/21—2/25    | Ch7. Instruction Set Architectures<br>Machine Language, Assemblers/Compilers |
| 7           | 2/28—3/4     | Ch8. CPU   |
| 8           | 3/7—3/11     | <b>Reviews</b><br><b>Midterm</b>   |
| 9           | 3/14—3/18    | <b>Spring Recess</b>   |
| 10          | 3/21—3/25    | Ch8. CPU<br>Ch9. Control Unit  |
| 11          | 3/28—4/1     | Ch9. Control Unit  |
| 12          | 4/4—4/8      | Ch10. Primary Memory   |
| 13          | 4/11—4/15    | Ch11. Input/Output<br>Secondary Storage                                      |
| 14          | 4/18—4/22    | Ch12. Pipelining<br>Multicore, GPU,  |
| 15          | 4/25—4/29    | Parallel Processing, Operating Systems...<br><b>Reviews</b>                  |
| 16          | 5/2—5/3      |  |
|             | 5/5—5/12     | <b>Final Examination</b><br>May 5 (Thursday), 2:00pm-4:00pm, MH 301          |